

FIG 24

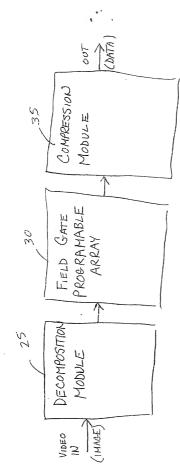
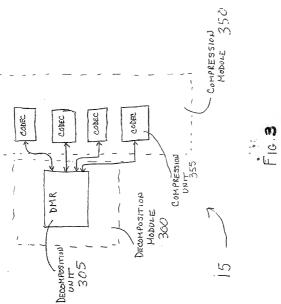
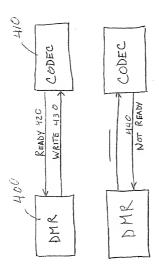
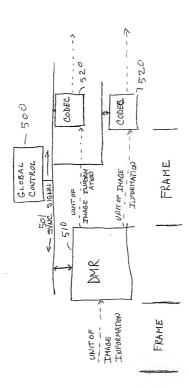


FIG. 28



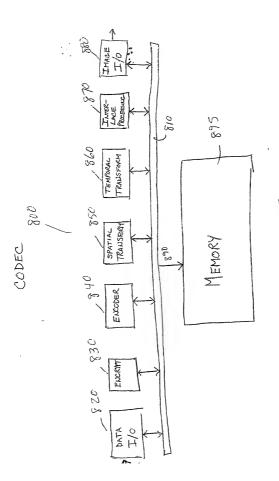


F1G. 4



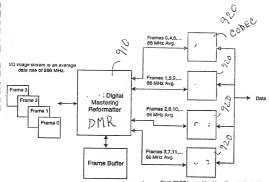
ON EACH STATE CLOCKED SYDE SIGLAR.
ONE UNIT OF IMAGE INFORMATION IS TRANSMITTED IN

F1G. 5



F1G.

. . .



Each ODMR is capable of handling 533 MHz I/O bandwidth and 133 MHz internal processing bandwidth.

Each QMIC is capable of handling 133 MHz I/O bandwidth and 66 MHz internal processing bandwidth.

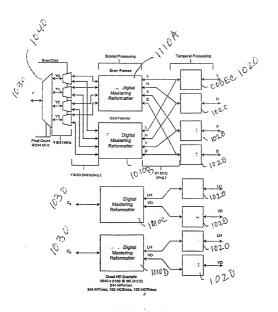


FIG. 19